## **JUXTA D Series** General **Specifications**

Model DA5 DISTRIBUTOR (with Square Root Extractor)

**NTXUL** 

## 1. General

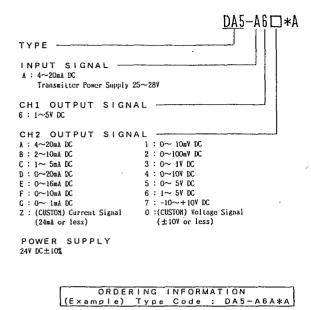
This DCS correspondence nest stored type distributor (with square root extractor) converts differential pressure flow signals to linear signals combining with 2-wire transmitter.

- Incorporation of transmitter short-circuit protection, low cut circuit and one-chip micro-computer provide high efficiency and superior performance.
- Zero and span adjustment, I/O monitoring, etc. can easily be made even in the field by upper system or handy terminal.

| . Specifications                 |  |  |  |  |
|----------------------------------|--|--|--|--|
| Input &                          | Output   |  |  |  |
| Input signal                     | 4∼20mA DC from 2-wire transmitter  |  |  |  |
| Input resistance                 | 250 Ω  |  |  |  |
| Permissible applied current      | 40mA or less   |  |  |  |
| Transmitter power supply voltage | 26.5±1.5V DC   |  |  |  |
|                                  | (with current limit circuit, limit at 25~35mA                            |  |  |  |
| Conductor resistance             | $RL \leq (20$ -transmitter minimum operating voltage) $V/0.02A(\Omega)$  |  |  |  |
| Input compensating function      | Extraction computation $Y = 2\sqrt{X-1+1(Y)}$                            |  |  |  |
|                                  | (Y:output signal, X:input signal)  |  |  |  |
| Chl output signal                | 1∼5V DC Low cut point: 0.6%  |  |  |  |
| Ch2 output signal                | DC voltage or current signal (In case of current output, output          |  |  |  |
| ·                                | is only available either from front terminal ③~④ or connector            |  |  |  |
|                                  | Low cut point : 0.6%   |  |  |  |
| Zero point adjustment range      | $\pm$ 1% of span (input adjust), $\pm$ 10% of span (output correction)   |  |  |  |
| Span adjustment range            | $\pm 1\%$ of span (input adjust), $\pm 10\%$ of span (output correction) |  |  |  |
| Standar                          | d Performance  |  |  |  |
| Accuracy rating                  | ±0.1% of span  |  |  |  |
| Response speed                   | 200ms 63% response (10~90%)s   |  |  |  |
| Insulation resistance            | 100MΩ or more (at 500V DC) between input~output,                         |  |  |  |
|                                  | input~power supply and output~power supply                               |  |  |  |
| Voltage withstand                | 1500V AC/minute between input~output, input~power supply                 |  |  |  |
|                                  | 500V AC/minute between output~power supply                               |  |  |  |
| Ambient temperature and          | Normal operating condition : 0~50°C, 5~90% RH                            |  |  |  |
| humidity                         | Operating limit: -10~60°C, 5~95% RH                                      |  |  |  |
|                                  | Storage condition: -10~70°C', 5~95% RH (no condensation)                 |  |  |  |
| Power supply voltage             | 24V DC±10% (ripple content 5% p-p or less)                               |  |  |  |
| Effect of power supply           | ±0.1% or less of span per 24V DC±10% fluctuation                         |  |  |  |
| voltage fluctuation              |  |  |  |  |
| Effect of ambient tempearture    | ±0.2% or less of span per 10°C temperature change                        |  |  |  |
| change                           |  |  |  |  |
| Current dissipation              | 24V DC 110mA (4~20mADC), 75mA (1~5VDC)                                   |  |  |  |
|                                  | g & Dimension  |  |  |  |
| Boards                           | Both sides glass-epoxy   |  |  |  |
| Mounting method                  | Store in exclusive nest (signal power supply be connected                |  |  |  |
|                                  | through back board and connector)  |  |  |  |
| Wiring                           | External wiring : Connect to terminal M4 screw of input/output           |  |  |  |
| (                                | of exclusive nest.   |  |  |  |
|                                  | Connection to I/O card: By exclusive cable (connector)                   |  |  |  |
| External dimension               | 130.6 X 23.6 X 126mm (HxWxD)   |  |  |  |
| Neight About 120g                |  |  |  |  |
| Accesso                          | ries .   |  |  |  |
| Tag number label 4               |  |  |  |  |



GS JD44-01E 2nd Edition : Sep. 2004(KP)

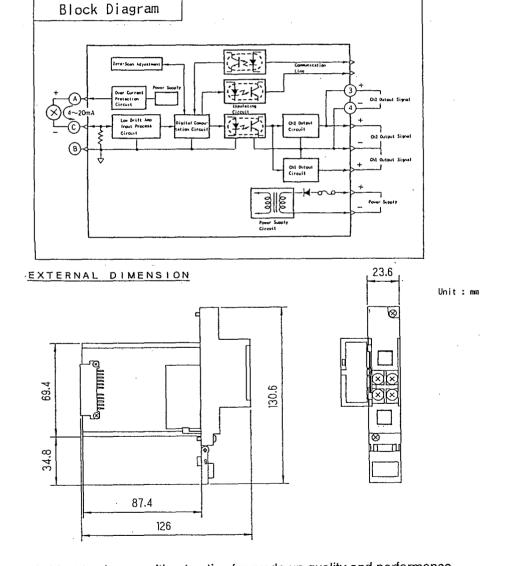


Output resistance and permissible load resistance

| (DC current output type) |                   |                             |  |  |  |
|--------------------------|-------------------|-----------------------------|--|--|--|
| Output signal            | Output resistance | Permissible load resistance |  |  |  |
| 4~20mA DC                |                   | 0~ 750 Ω                    |  |  |  |
| 2~10mA DC                |                   | 0~1500Ω                     |  |  |  |
| 1∼ 5mA DC                | 5XΩ or more       | 0~3000Ω                     |  |  |  |
| 0~20mA DC                | 1                 | 0~ 750Ω                     |  |  |  |
| 0~16mA DC                |                   | 0~ 900Ω                     |  |  |  |
| 0~10mA DC                |                   | 0~1500Ω                     |  |  |  |
| O∼ 1mA DC                | 1                 | 0~ 15kΩ                     |  |  |  |
| Others, in case of       | 1                 | (15/1)Ω or less             |  |  |  |
| 1 = 24mA or less         |                   |                             |  |  |  |
|                          |                   | i=100% output current valu  |  |  |  |

|                    | (DC voltage | output ty         | /pe)    | ·                           |
|--------------------|-------------|-------------------|---------|-----------------------------|
| Output signal      |             | Output resistance |         | Permissible load resistance |
| 0~ 10m¥ DC         |             | 100Ω or less      |         | 250KΩ or more               |
| 0~100mV DC         |             |                   |         |                             |
| 0~ 1V DC           |             |                   |         | 2KΩ or more                 |
| 0~ 10V DC          |             |                   |         | 10KΩ or more                |
| 0∼ 5¥ DC           |             | lΩ                | or less | 2KΩ or more                 |
| 1∼ 5V DC           |             |                   |         | 2KΩ or more                 |
| -10~+10V DC        |             |                   |         | 10KΩ or more                |
| Others, in case of | V≦100mV     | 100Ω              | or less | 250KΩ or more               |
| 1,=10V or less     | V>100mV     | 1Ω                | or less | 10KΩ or more                |
|                    |             |                   |         | V=100% output voltage value |

(Note) At  $0\sim$ XmA of current output type, output value of 0.5% or less would be out of warranty regarding relative accuracy for Chl output.



Subject to change without notice for grade up quality and performance